

Notice of References Cited

Application/Control No.

10/029,593

Applicant(s)/Patent Under

Reexamination

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Examiner

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Art Unit

2836

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
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	D	US-5,760,571	06-1998	Latham et al.	323/274
	E	US-5,587,685	12-1996	Johansson	327/546
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	R. Boylestad, L. Nashelsky, Electronic Devices and Circuit Theory, Prentice Hall, 1987, pp.692 -693.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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